

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicants: R. Bhattacharya et al.  
Case: 1-4-2-2-1  
Serial No.: 10/620,045  
Filing Date: July 15, 2003  
Group: 2123  
Examiner: Juan Carlos Ochoa

Title: Method and Apparatus for Automatic Generation  
of Multiple Integrated Circuit Simulation Configuration

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**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the non-final Office Action dated November 3, 2009, Applicants respectfully request reconsideration of the above-identified application in view of the amendments and remarks herein.